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**Silicon Valley office:**  
2099 Gateway Place, Suite 320  
San Jose, CA 95110-1017  
Telephone: (408) 451-5900  
Facsimile: (408) 451-5908

The Law Offices of  
Bever, Hoffman & Harms, LLP

**Tri-Valley office:**  
1432 Concannon Blvd., Bldg. G  
Livermore, CA 94550  
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# Fax

**To:** OIPE, Filing Receipt Corrections **From:** Patrick T. Bever, Esq.  
Ph: (408) 451-5902  
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**Fax:** (703) 746-9195 **Pages:** 4 (including Fax coversheet)

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**Phone:** (703) **Date:** November 10, 2004

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**Re:** Serial No.: 10/712,473 **CC:**  
Atty. Dkt. No.: 2002 P 50207 US (INF-030)

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CONFIDENTIAL

Attorney Docket No. 2002 P 50207 US (INF-030)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Robert E. Ober et al. ) Group Art Unit: 2111  
Serial Number: 10/712,473 ) Examiner: Mark H. Rinchart  
Filing Date: 11/12/2003 )  
Title "Interrupt And Trap Handling In An Embedded Multi-Thread Processor To Avoid  
Priority Inversion And Maintain Real-Time Operation")

Assistant Commissioner for Patents  
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REQUEST FOR CORRECTED FILING RECEIPT

Sir:

There are errors with respect to the following, which are misspelled, or should be omitted:

<u>Error in</u>	<u>Correct data</u>
Applicant(s) names	Daniel F. Martin

Issuance of a corrected Filing Receipt is respectfully requested.

Dated: Nov. 10, 2004 By: Patrick T. Bever  
Respectfully submitted,  
Patrick T. Bever, Reg. No. 33,834

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I hereby certify that, on the date shown below, this correspondence is being transmitted by facsimile to the Patent and Trademark Office.

Date: 11/10/2004 Signature: Robert E. Ober

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/712,473	11/12/2003	2189	1230	2002 P 50207 US	9	36	5

CONFIRMATION NO. 9335

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FILING RECEIPT



\*OC000000011852531\*

Date Mailed: 02/09/2004

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## Applicant(s)

Robert E. Ober, San Jose, CA;  
 Roger D. Arnold, Sunnyvale, CA;  
 Daniel F. Martin, Mountain View, CA;  
 Erik K. Norden, Unterhaching, GERMANY;

DANIEL →

## Assignment For Published Patent Application

Infineon Technologies North America Corp.;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted: 02/09/2004

Projected Publication Date: 05/12/2005

Non-Publication Request: No

Early Publication Request: No

Title

Interrupt and trap handling in an embedded multi-thread processor to avoid priority inversion and maintain real-time operation

**Preliminary Class**

710

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